

LIST OF CURRENT CLAIMS

1. (Currently Amended) A basic input/output system updating method comprising the steps of:

dividing a memory in an electronic device into a system program area and a boot program area,

storing a basic ~~input/output~~ input/output system (BIOS) program in the said system program area such that when a microprocessor of the said electronic device reads the said program, the said microprocessor executes a system initialization sequence of the said electronic device;

storing an initialization sequence into said boot program area, ~~while the~~ wherein ~~when~~ said system program area ~~be is~~ destroyed and the BIOS program is lost, ~~a switch is able to be switched to an ON position to connect the said microprocessor, connectable to the said boot program area and thereby enables the~~ via a switch, enables reading of the said initialization sequence in the said boot program area.

2. (Currently Amended) ~~As mentioned in Claim 1 of the~~ The basic input/output system updating method according to Claim 1, wherein the said microprocessor, during the said initialization sequence, activates the hard disk drive connected to the said electronic device, reads the said BIOS program stored in the said hard disk drive, and records the said BIOS into the said system program area.

3. (Currently Amended) ~~As mentioned in Claim 1 of the~~ The basic input/output system updating method according to Claim 1, wherein the said microprocessor during the said initialization sequence, activates an integrated drive electronics (IDE) interface of the said electronic device, reads a BIOS program stored in the said IDE-interface connected to the said hard disk drive, and records the said BIOS read from the said IDE-interface into the said system program area.

4. (Currently Amended) ~~As mentioned in Claim 1 of the~~ The basic input/output system updating method according to Claim 1, ~~the~~ said microprocessor, during ~~the~~ said initialization sequence, links with a local area network (LAN), downloads a BIOS program stored in ~~the~~ said LAN, and records ~~the~~ said BIOS downloaded from ~~the~~ said LAN into ~~the~~ said system program area.

5. (Currently Amended) ~~As mentioned in Claim 1 of the basis~~ The basic input/output system updating method according to Claim 1, wherein ~~the~~ said electronic device is a notebook computer.

6. (Currently Amended) ~~As mentioned in Claim 5 of the~~ The basic input/output system updating method according to Claim 5, wherein ~~the~~ said memory is a flash read-only memory (ROM).

7. (Currently Amended) ~~As mentioned in Claim 5 of the~~ The basic input/output system updating method according to Claim 5, wherein ~~the~~ said switch is a manually operated shunting component.

8. (Currently Amended) ~~As mentioned in Claim 5 of the~~ The basic input/output system updating method according to Claim 5, wherein when the power supply of ~~the~~ said electronic device is switched on, ~~the~~ said microprocessor issues an address signal via a decoder which is sent after decoding to ~~the~~ said system program area and ~~the~~ said boot program area~~[[,]]~~ ~~thereby enabling the~~ to enable said microprocessor to respectively assign the starting position of ~~the~~ said system program area and ~~the~~ said boot program area.

9. (Currently Amended) ~~As mentioned in Claim 2, of the basis~~ The basic input/output system updating method according to Claim 2, wherein ~~the~~ said electronic device is a notebook computer.

10. (Currently Amended) ~~As mentioned in Claim 3, of the basis~~ The basic input/output system updating method according to Claim 3, wherein ~~the~~ said electronic device is a notebook computer.

11. (Currently Amended) ~~As mentioned in Claim 4, of the basis~~ The basic input/output system updating method according to Claim 4, wherein ~~the~~ said electronic device is a notebook computer.